

AMENDMENTS TO THE CLAIMS

Please ADD claims 7-17, as shown below.

The following is a complete list of all claims in this application.

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1. (Original) A liquid crystal display device, comprising:  
dual bank type source driver PCBs installed at top and bottom of a liquid crystal panel;  
a gate driver PCB;  
a staple-shaped main PCB formed at the back of the liquid crystal panel; and  
a timing controller mounted at the main PCB to process signals input from outside and  
generate driving signals, the main PCB transmitting the relevant driving signals to the respective  
source driver PCBs and the gate driver PCB.
2. (Original) The liquid crystal display device of claim 1, wherein the staple-shaped  
main PCB has a top portion and a bottom portion proceeding in the horizontal direction and a  
side portion proceeding in the vertical direction, and the top portion and the bottom portion of  
the main PCB axially meet the side portion of the main PCB at a predetermined angle except for  
a right angle.
3. (Original) The liquid crystal display device of claim 2, wherein the top portion  
and the bottom portion of the staple-shaped main PCB have an axial length of one half or more  
of the liquid crystal panel.
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4. (Original) The liquid crystal display device of claim 2, wherein the timing controller is positioned at the side portion of the staple-shaped main PCB.

5. (Original) The liquid crystal display device of claim 2, wherein the top portion and the bottom portion of the staple-shaped main PCB are respectively connected to the corresponding source driver PCBs via one or more FPCs to transmit the relevant driving signals to the source driver PCBs.

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6. (Original) The liquid crystal display device of claim 2, wherein the side portion of the staple-shaped main PCB is connected to the gate driver PCB via one or more FPCs to transmit the relevant driving signals to the gate driver PCB.

7. (Currently Added) A liquid crystal device (LCD) comprising:  
a liquid display panel having a front surface and a back surface;  
a gate driver printed circuit board (PCB) attached to the liquid crystal display panel;  
a first source driver printed circuit board (PCB) attached on a first edge portion of the back surface of the liquid crystal display  
a second source driver printed circuit board (PCB) attached on a second edge portion of the back surface of the liquid crystal display, the second edge portion facing the first edge portion; and  
a main printed circuit board (PCB) attached on the back surface of the liquid crystal panel, the main PCB comprising:

a timing controller generating a first source driving signal, a second source driving signal and a gate driving signal;

a first portion extending along the first source driver PCB and outputting the first source driving signal to the first source driver PCB; and

a second portion extending along the second source driver PCB and outputting the second source driving signal to the second source driver PCB.

8. (Currently Added) The LCD of claim 7, the main PCB further comprising a third portion extended between the first portion and the second portion, wherein the timing controller is mounted on the third portion.

9. (Currently Added) The LCD of claim 8, wherein the gate driver PCB is attached on a third edge portion of the back surface of the liquid crystal display.

10. (Currently Added) The LCD of claim 9, wherein the third portion of the main PCB is extending along the gate driver PCB and outputting the gate driving signal to the gate driver PCB.

11. (Currently Added) The LCD of claim 10, wherein the third portion of the main PCB is extending between end portions of the first portion and the second portion.

12. (Currently Added) The LCD of claim 7, further comprising a plurality of flexible printed cables (FPCs) transferring the first source driving signal from the first portion of the

main PCB to the first source driver PCB, the second source driving signal from the second portion of the main PCB to the second source driver PCB, and the gate driving signal from the main PCB to the gate driver PCB.

13. (Currently Added) The LCD of claim 7, wherein the liquid crystal panel is divided into first, second, third and fourth partitions arranged in a matrix of two rows and two columns.

14. (Currently Added) The LCD of claim 13, further comprising:

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a first flexible printed cable (FPC) transferring the first source driving signal for the first partition located on a first row and a first column from the first portion of the main PCB to the first source driver PCB;

a second flexible printed cable (FPC) transferring the first source driving signal for the second partition located on the first row and a second column from the first portion of the main PCB to the first source driver PCB;

a third flexible printed cable (FPC) transferring the second source driving signal for the third partition located on a second row and the first column from the second portion of the main PCB to the second source driver PCB; and

a fourth flexible printed cable (FPC) transferring the second source driving signal from the fourth partition located on the second row and the second column from the second portion of the main PCB to the second source driver PCB.

15. (Currently Added) The LCD of claim 7, wherein the liquid crystal panel is divided into a plurality of partitions.

16. (Currently Added) The LCD of claim 15, further comprising:  
a plurality of first flexible printed cables (FPCs) transferring the first source driving signal for the partitions arranged on an upper portion of the liquid crystal panel from the first portion of the main PCB to the first source driver PCB; and  
a plurality of second flexible printed cables (FPCs) transferring the second source driving signal for the partition arranged on the lower portion of the liquid crystal panel from the second portion of the main PCB to the second source driver PCB.

17. (Currently Added) The LCD of claim 15, further comprising:  
at least one third flexible printed cable (FPC) for transferring the gate driving signal for the partitions arranged on an upper portion of the liquid crystal panel from the main PCB to the gate driver PCB; and  
at least one fourth flexible printed cable (FPC) for transferring the gate driving signal for the partitions arranged on a lower portion of the liquid crystal panel from the main PCB to the gate driver PCB.